

FIG. 1

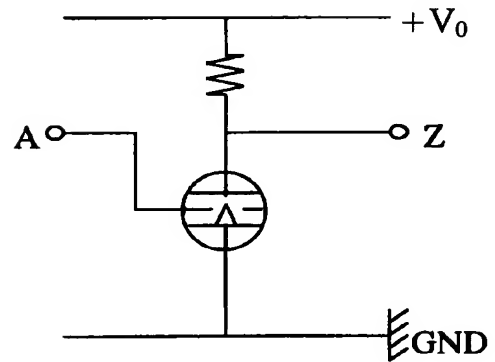
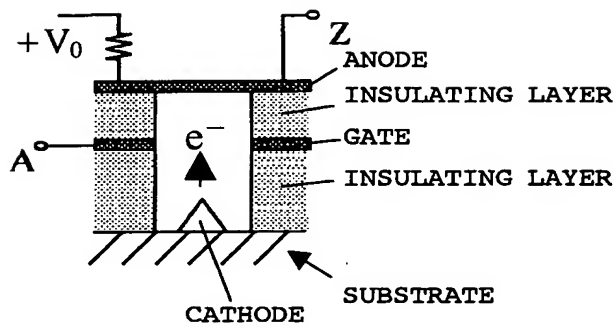


FIG. 2

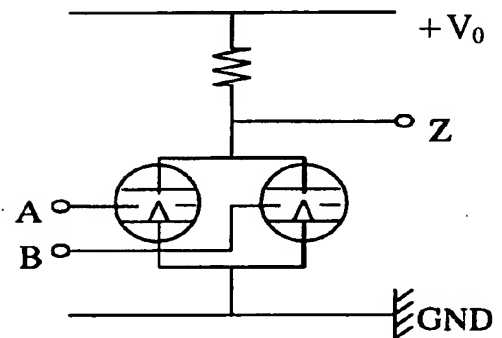
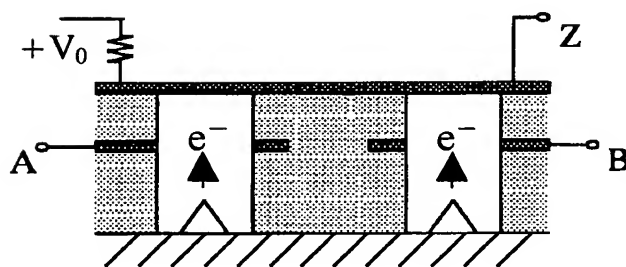


FIG. 3

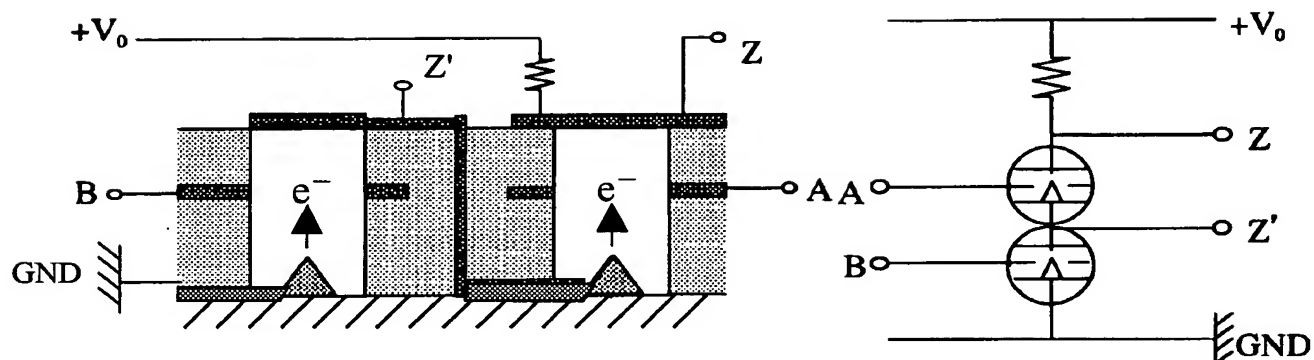
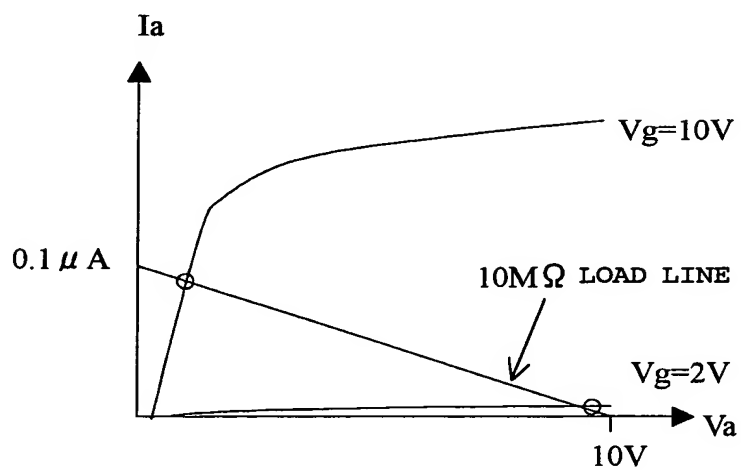


FIG. 4



I-V CURVE OF DIAMOND EMITTER

The diagram illustrates a two-electron transistor (2ET) structure. It features a central island (island 1) and two side islands (islands 2 and 3). Island 1 is connected to a gate voltage $+V_0$ and a terminal Z. Islands 2 and 3 are connected to terminals B and A, respectively. The bottom of the device is grounded (GND). Electrons (e^-) are shown moving from the islands towards the ground.

FIG. 7

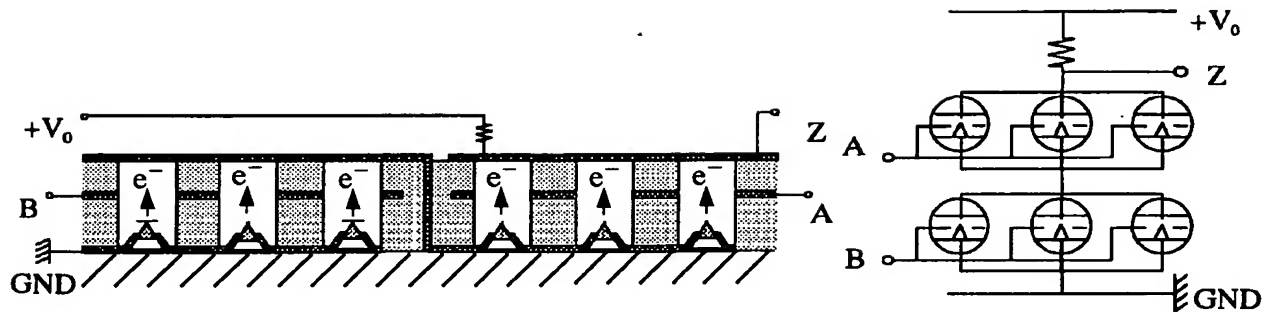


FIG. 8

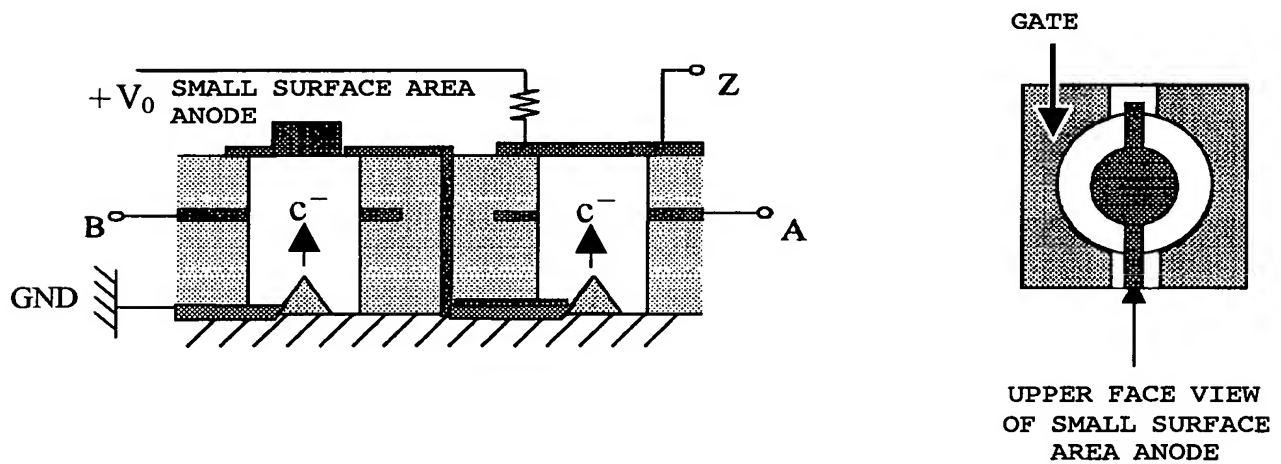


FIG. 9

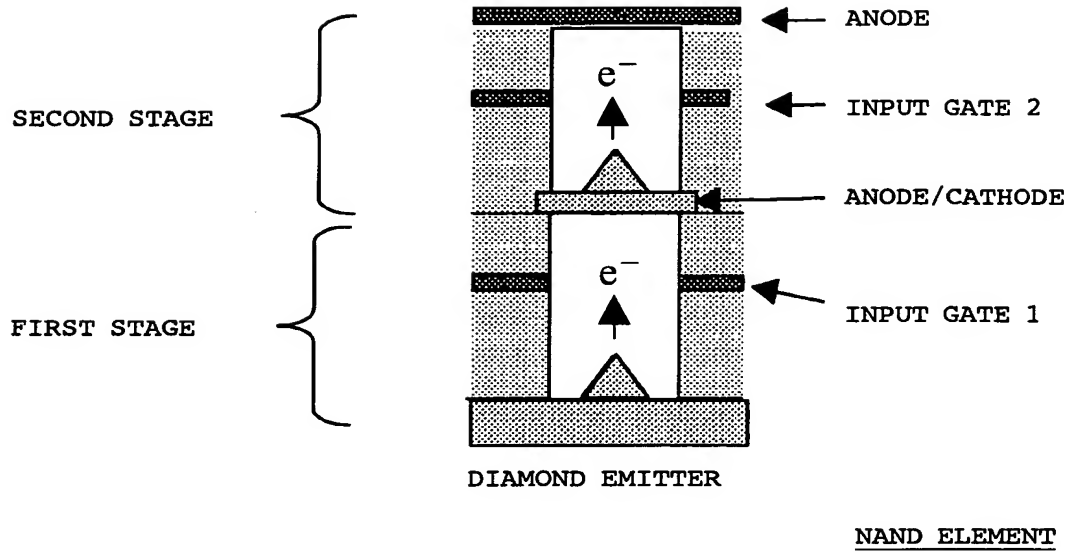
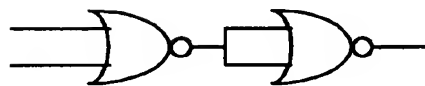


FIG. 10



$$A + B = \overline{\overline{A + B}}$$

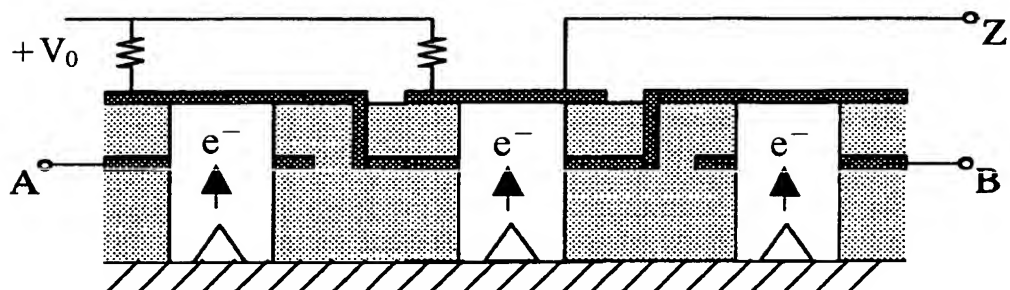


FIG. 11

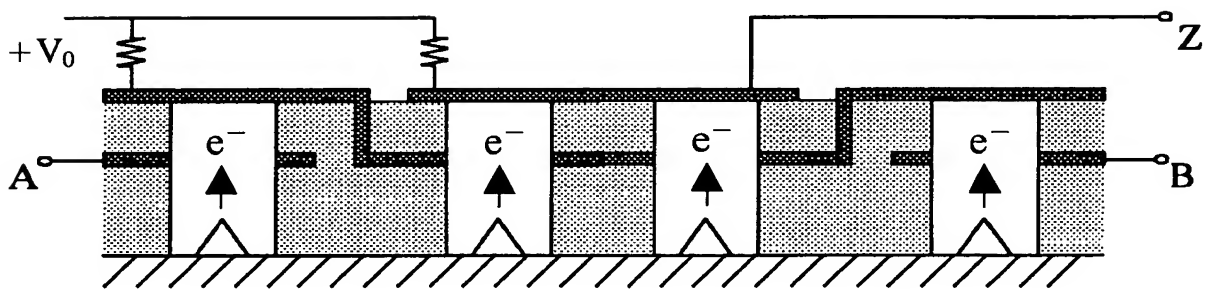
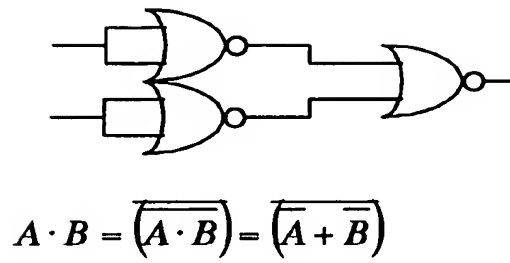


FIG. 12

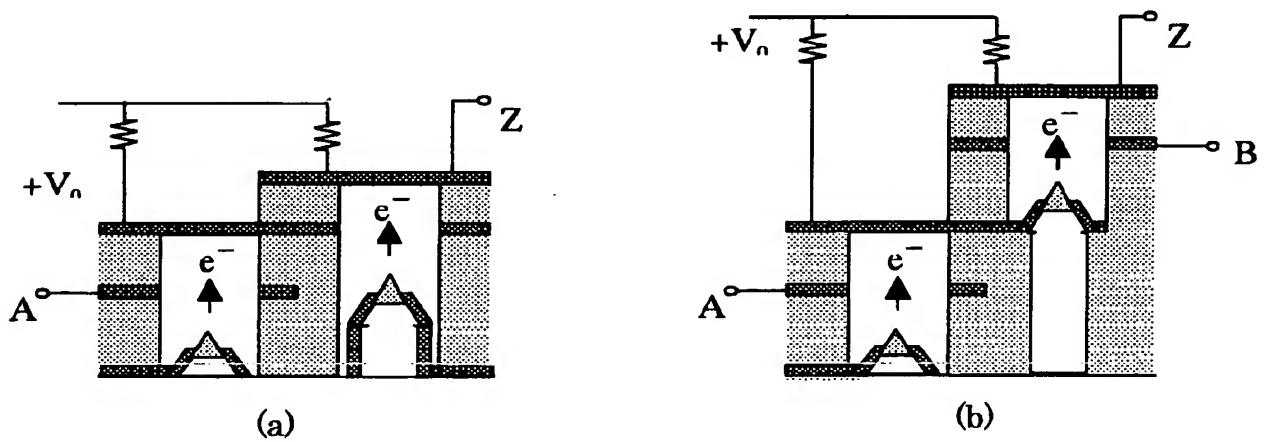


FIG. 13

